EE|Times europe
Global news for the creators of technology

european business press

APPLY NOW
FOR A FREE
SUBSCRIPTION

EETIMES europe
News and Analysis for the **Creators of Technology**
Subscribe today   www.eetimes.eu

EETimes.com | EETimes.eu | About | Contact | Feedback | RSS | **Newsletter** | Media Kit | Calendar of Events

Search

Regional Editions: Germany   France   UK   Scandinavia   Israel   Eastern Europe   Russia

Renew / Update your subscription

NEWS BY TOPIC

Automotive
Communications
Consumer
Design & IP
Embedded
Environment
Industrial
Power
Production
Research
Semiconductor
Test & Measurement
Wireless

PRODUCTS

POWERED BY EEXPRESS

Analog ICs
Boards/Buses
DSP
Displays
Electromechanical
Embedded Tools
Interconnect
Logic & Interface
Memory
MPUs/MCUs
Passives/Sensors
PLDs/FPGAs
Power Components
Power Sources
RF/Microwave
Test/Measurement

Technical Papers

Strukturierte ASICs der zweiten Generation lösen zunehmend FPGAs ab

Nextreme Structured ASICs

UML for C
    All White Papers »

Podcasts

Interview With Bruce

European Nanoelectronics project seeks more proposals

Colin Holland
EE Times Europe
02/08/2010 7:42 AM

Print
Email
Reprints
SHARE

LONDON — CATRENE, the industry-driven Cluster for Application and Technology Research in Europe on NanoElectronics (EUREKA 4140) has announced its third call for project proposals from Feb 1 to June 1, 2010.

Participation in the project outline phase is mandatory until March 18 for participation in the subsequent full proposal phase. After the evaluation and selection process, the final decision can be expected on June 30 with project labelling. It can be assumed that the projects will start from January 2011.

With already 21 active projects resulting from the first and second call, involving 300 participating organisations from 16 European countries and almost 7000 person-years of researchers, CATRENE is the leading European programme in nanoelectronics supported by industry and governments.

Keep abreast of the latest industry news with EE Times' newsletters Register now!
EETimes europa

CATRENE is a EUREKA program that aims at a technological leadership for a competitive European ICT industry. CATRENE first phase is a four-year programme, started Jan 1, 2008, and is extendable by another four years. This is in line with the changing landscape of the semiconductor industry as well as the present view on technology evolution and the time span over which most of the major applications will develop. Resources required will be approximately 2,500 person-years annually, equalling about €4 billion for the extended program.

CAREER CENTER

Overworked and under-appreciated?
Open | **Close**

RELATED ARTICLES

- Low-pin-count MCUs with on-chip flash memory
- Korea's Advanced Digital Chips joins EEMBC board of directors
- Open source RTOS makes debut on the AVR
- Industrial server has 4 PCI Express x4/x16 expansion slots
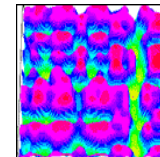- Electronic drive with a fully programmable control unit

Around EE Times Europe

Win Anders Electronics' evaluation kit for its latest 5.7" touchscreen TFT LCD display
This month, Anders Electronics is giving away a full evaluation kit of its recently released 5.7inch UMR 5 intelligent display unit, for one of EE Times Europe's readers to win.

Prize winners of our previous reader offer
In the January edition of EE Times Europe, NEC Electronics was giving away five Five V850/Jx3-E Network-It! supporting Ethernet, USB, and CAN from a V850ES 32-bit MCU based device. **And the winners are...**

European Nanoelectronics project seeks more proposals
CATRENE, the industry-driven Cluster for Application and Technology Research in Europe on NanoElectronics (EUREKA 4140) has announced its third call for project proposals from Feb 1 to June 1, 2010.

Engineers explore life beyond 10 Gbit links
Electronics engineers are bumping up against the limits of their tools and techniques—and perhaps even physics--to keep pace with the rapidly expanding needs of an Internet-driven society as they coalesce around a move to 25 Gbit/second chips and ponder what comes next.